## An Automatic Test System for Schottky UFRGS **Diode Variability Characterization** UFRGS

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This brings light paper to the development of a system to measure characterize custom designed and electronic circuits. This system is developed with a particular circuit in mind, a Schotkky diode matrix [3], but can be generalized to the use of several other different circuits like digital-analog (DA) converters for and even radio-frequency (RF) applications.

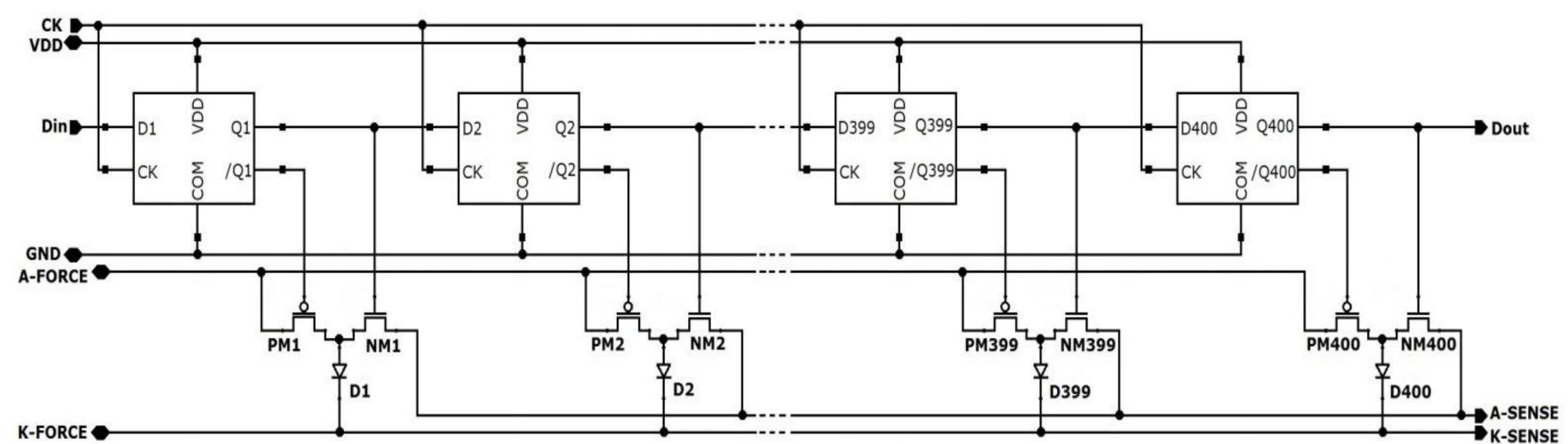
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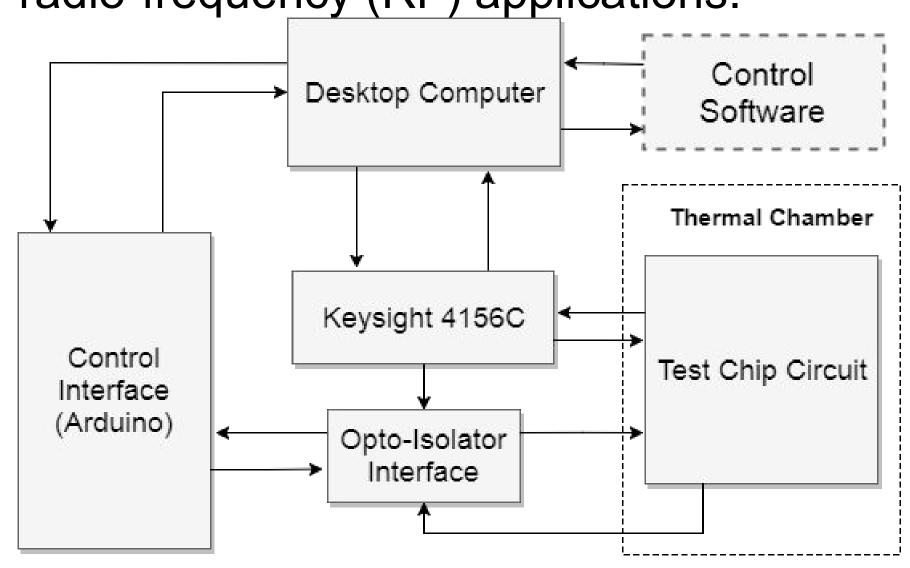
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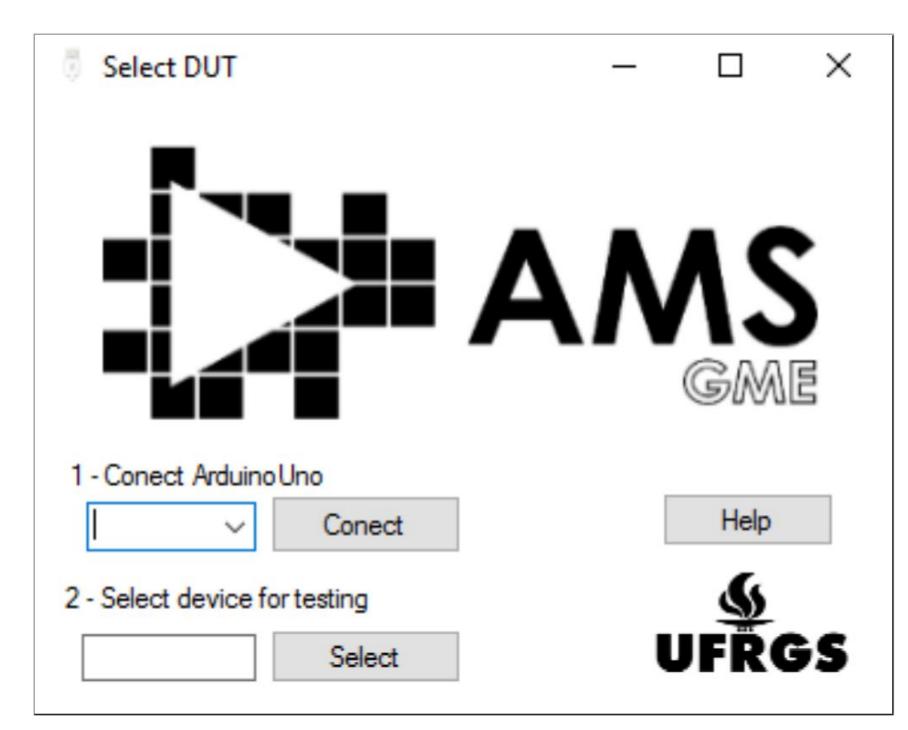


**Structure** – It consists of a parameter analyzer being connected to a test conform chamber, made to the

K-FORCE **Test Chip** – The schottky diodes are **Preliminary Tests** – The tests were selected by CMOS switches controlled by programmable shift-registers. These are controlled by he Control Interface through the Opto-isolator interface.

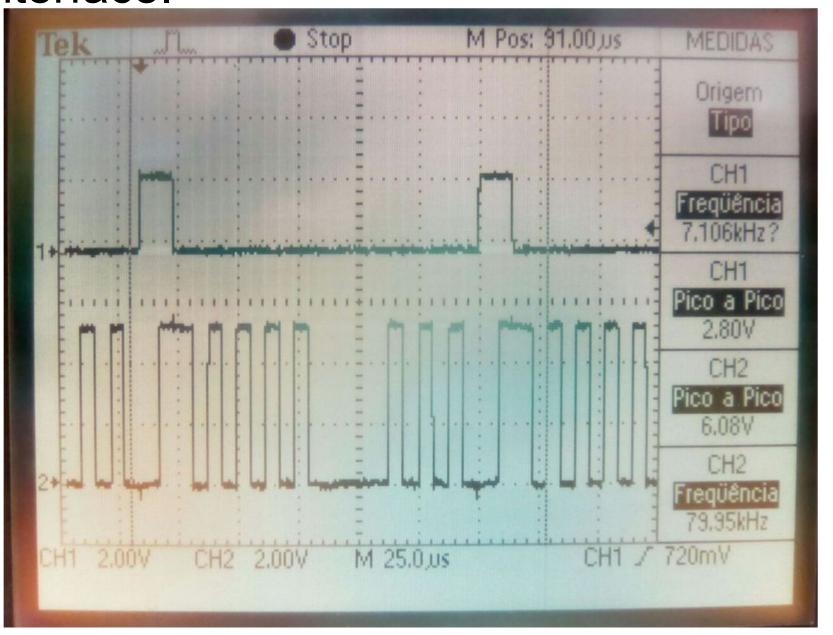
**Control Interface** – The definition of the device to be tested, as well as the Arduino interface and verification of the vector signal sent are made by the control interface. It features a GUI to

assist the user.



performed in a modular way, here presented the opto-isolator interface while with integrated the control interface.

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Further Work – Integration of the performing structure of and measurements to characterize the developed Schottky diode matrix. Acknowledgment – Thank you to Propesq/UFRGS, UFRGS Engineering School, FAPERGS, GME AMS-RF, CNPq and CAPES brazilian agencies.

environment to the desired needs, an isolator between the measured chip and desktop computer system, a microcontoller system for interpreting routines and the desktop computer itself interpret routines which the Will developed for the system.

## References

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